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Docket No. F-6810



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kenji SHIGEKI, et al.
Serial No. : 09/772,027
Filed : January 29, 2001
For : LOGIC INTEGRATED CIRCUIT, AND RECORDING
MEDIUM READABLE BY A COMPUTER, WHICH
STORES A SOURCE OF CPU CORE ON SAID LOGIC
INTEGRATED CIRCUIT
Group Art Unit : 2183
Examiner : William M. Treat
Confirmation No. : 9064
Customer No. : 000028107

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SEP 03 2004

Technology Center 2100

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MAIL STOP NON-FEE AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

09/28/2004 NHALL1 00000002 101250 09772027

RESPONSE

01 FC:1251 11:00 DA
Sir:

In response to the Office Action of June 30, 2004, the following is
submitted: